

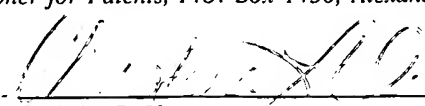


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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Christina L. Vann

Applicant : Ian S. Eslick, et al. Confirmation No. 2759
Application No. : 10/694,282
Filed : October 27, 2003
Title : SYSTEM AND METHOD FOR EXECUTING HYBRIDIZED CODE ON
A DYNAMICALLY CONFIGURABLE HARDWARE ENVIRONMENT

Grp./Div. : 2183
Examiner : Thomas C. Lee

Docket No. : 51346/RRT/B600

**INFORMATION DISCLOSURE STATEMENT AND
CERTIFICATION UNDER § 1.97(e)(1)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Post Office Box 7068
Pasadena, CA 91109-7068
January 25, 2005

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO/SB/08A/B with a listing of references that are known to applicant. Copies of each of the listed references are enclosed.

It is respectfully requested that these references be considered in the examination of this application and identified on the list of references cited on the patent issuing on this application. Applicant also requests that an initialed copy of said FORM PTO/SB/08A/B be entered in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

Applicant's undersigned attorney hereby certifies, in accordance 37 CFR § 1.97(e)(1), that each item of information contained in the information disclosure statement was first cited in any


Application No. 10/694,282

communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By



Raymond R. Tabandeh

Reg. No. 43,945

626/795-9900

RRT/clv

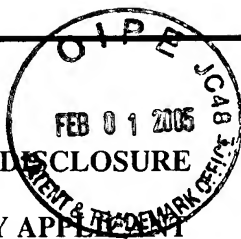
Enclosures: PTO/SB/08A/B w/references

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FORM PTO/SB/08A/B (10-01)
Substitute for PTO-1449A/B

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)



Attorney Docket Number	51346/DBP/B600
Application Number	10/694,282
Filing Date	October 27, 2003
Applicant(s)	Ian S. Eslick, et al.
Group Art Unit	2183
Examiner Name	Thomas C. Lee

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - Kind Code ² (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (✓)

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		BREBNER, Gordon, "A Virtual Hardware Operating System for the Xilinx XC6200", Field-Programmable Logic and Applications, 6th International Workshop, September 23-25, 1996, pp. 327-336, XP008037568
		JEAN, Jack S.N., et al., "Dynamic Reconfiguration to Support Concurrent Applications", IEEE Transactions on Computers, Vol. 48, No. 6, June 1999, pp. 591-602, XP-000832606
		LEE, Ming-Hau, et al., "Design and Implementation of the MorphoSys Reconfigurable Computing Processor", Journal of VLSI Signal Processing Systems, March 2000, pp. 147-162, Kluwer Academic Publishers, The Netherlands, XP-000908462
		European Search Report for European Patent Application No. EP 01 25 0247 dated December 7, 2004, 2pp.

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EXAMINER SIGNATURE		DATE CONSIDERED	
<small>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.pto.gov or MPEP 901.4. ³Enter Office that issued the document, by the two-letter code (WIPO standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English Language Translation is attached.</small>			

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